

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Prior Application: M. HAGIWARA et al  
Serial No. 10/023,660  
Filed: December 21, 2001

Group Art Unit: 2185  
Examiner: D. Butler  
For: DISK DRIVE AND COMPUTER

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

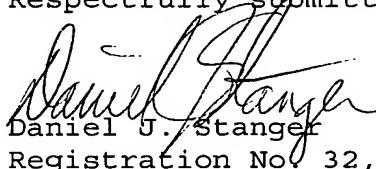
January 28, 2004

Sir:

In accordance with the duty of disclosure, the Applicants inform the Examiner of the documents cited during prosecution of the parent application, USSN 10/023,660.

The Applicants request the Examiner to initial and return a copy of the attached PTO-1449 form as an indication that the references have been considered.

Respectfully submitted,

  
Daniel J. Stanger  
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Attorney for Applicant(s)

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Date: January 28, 2004

FORM PTO-1449 (REV. 7-80)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. H-730-03	SERIAL NO.	
LIST OF DOCUMENTS CITED BY APPLICANT (Use several sheets if necessary)				APPLICANT M. HAGIWARA et al		
				FILING DATE 01/28/04	GROUP 2185	

## U.S. PATENT DOCUMENTS

* EXAMINER INITIAL		DOCUMENT	DATE	NAME	CLASS	SUBCLASS	FILING DATE (If Appropriate)
	AA	5,388,267	02/07/95	Chan et al			
	AB	5,737,765	04/07/98	Shigeeda			
	AC	5,481,713	01/02/96	Wetmore et al			
	AD	4,979,055	12/18/90	Squires et al			
	AE	5,579,522	11/1996	Christeson et al			
	AF	4,979,055	12/1990	Squires et al			
	AG						
	AH						
	AI						
	AJ						
	AK						

## FOREIGN PATENT DOCUMENTS

		DOCUMENT	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES	NO
	AL	02-081130	03/22/90	Japan			<input type="checkbox"/>	<input type="checkbox"/>
	AM	06-175829	06/24/94	Japan			<input type="checkbox"/>	<input type="checkbox"/>
	AN	524,719	01/27/93	Europe			<input type="checkbox"/>	<input type="checkbox"/>
	AO	07-064795	03/10/95	Japan			<input type="checkbox"/>	<input type="checkbox"/>
	AP	06-187141	07/08/94	Japan			<input type="checkbox"/>	<input type="checkbox"/>

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

AR	INTERFACE, Vol. 21, No. 8, August 1995, S. Watanabe et al, "Utilization of Microcomputer with Built-in Flash Memory", pp. 175-185.
AS	NIKKEI ELECTRONICS, published by Nikkei Business Publications, Inc.,
	June 6, 1994, pp. 67-96.
AT	Watanabe et al, "Learning about High-Speed/Multi-Port Communication through Fabrication", INTERFACE 95, Vol. 21-8 (219th issue), pp. 175-185, August 1995, CQ Publishing Co., Ltd.

EXAMINER	DATE CONSIDERED
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\* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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	AH						
	AI						
	AJ						
	AK						

**FOREIGN PATENT DOCUMENTS**

		DOCUMENT	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES <input type="checkbox"/> NO <input type="checkbox"/>
	AL	08-030450	02/02/96	Japan			<input type="checkbox"/> <input type="checkbox"/>
	AM	06-161929	10/06/94	Japan			<input type="checkbox"/> <input type="checkbox"/>
	AN						<input type="checkbox"/> <input type="checkbox"/>
	AO						<input type="checkbox"/> <input type="checkbox"/>
	AP						<input type="checkbox"/> <input type="checkbox"/>

**OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)**

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	AS		
	AT		
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